

Title: FEED-FORWARD CIRCUIT FOR REDUCING DELAY THROUGH AN INPUT BUFFER
Inventors: Becker et al.
App. No. 10/759,339 Docket No. ARTCP043

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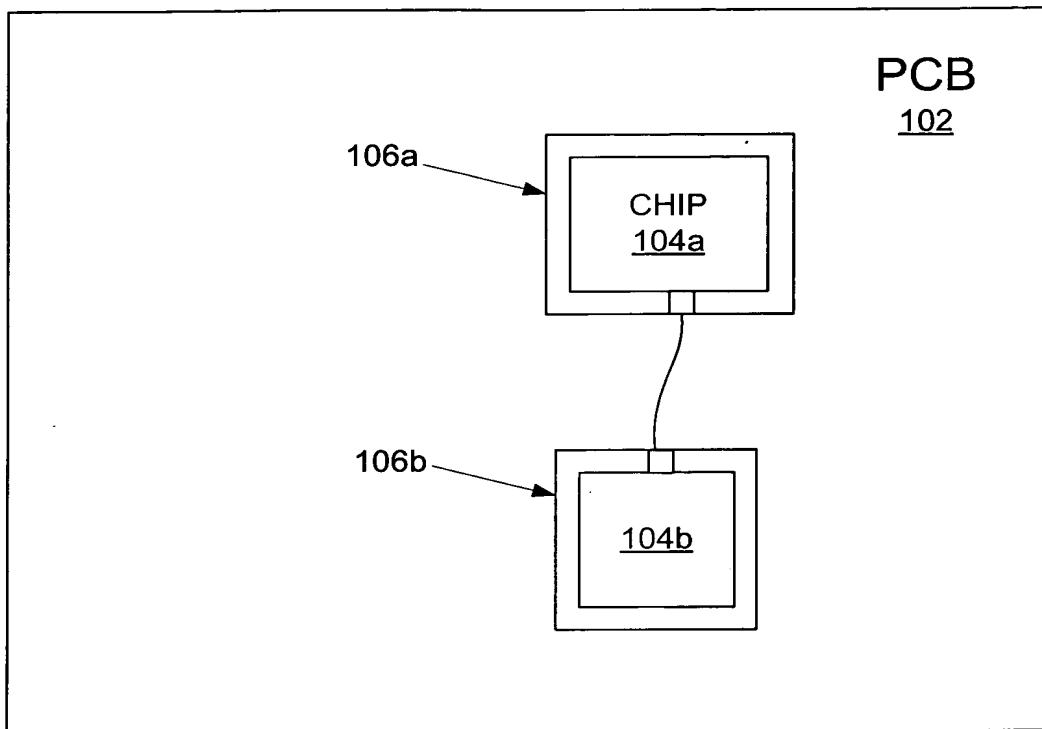


FIG. 1
(Prior Art)

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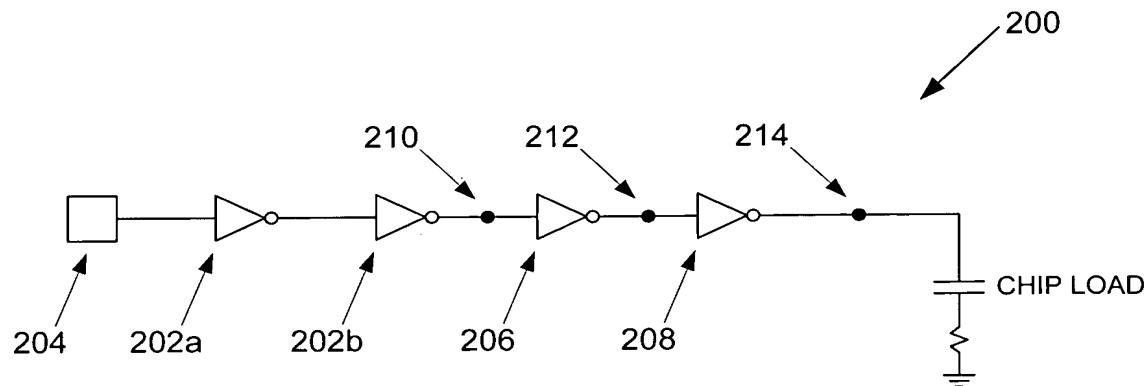


FIG. 2
(Prior Art)

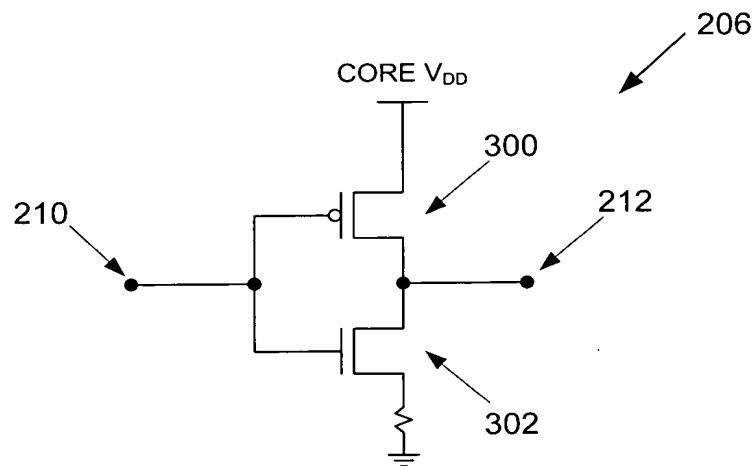


FIG. 3
(Prior Art)

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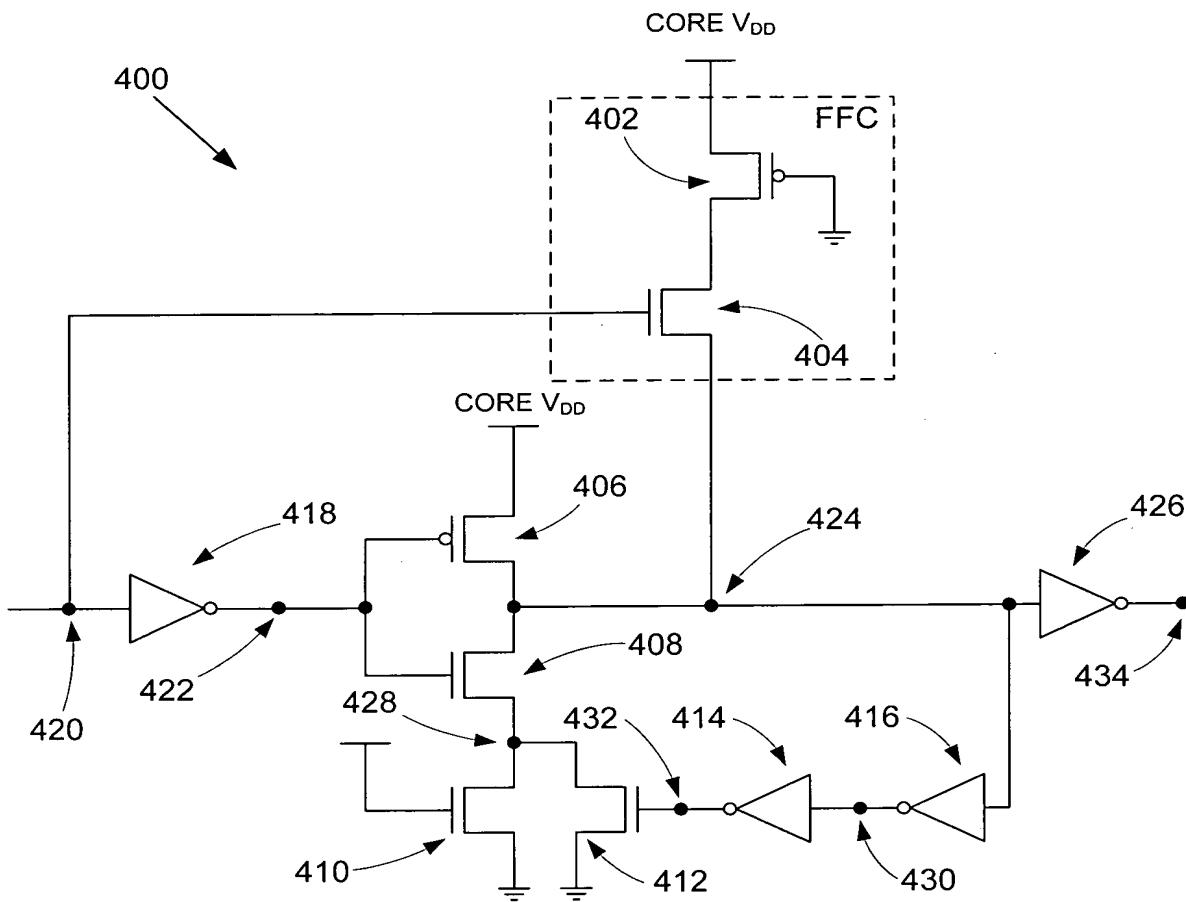


FIG. 4A

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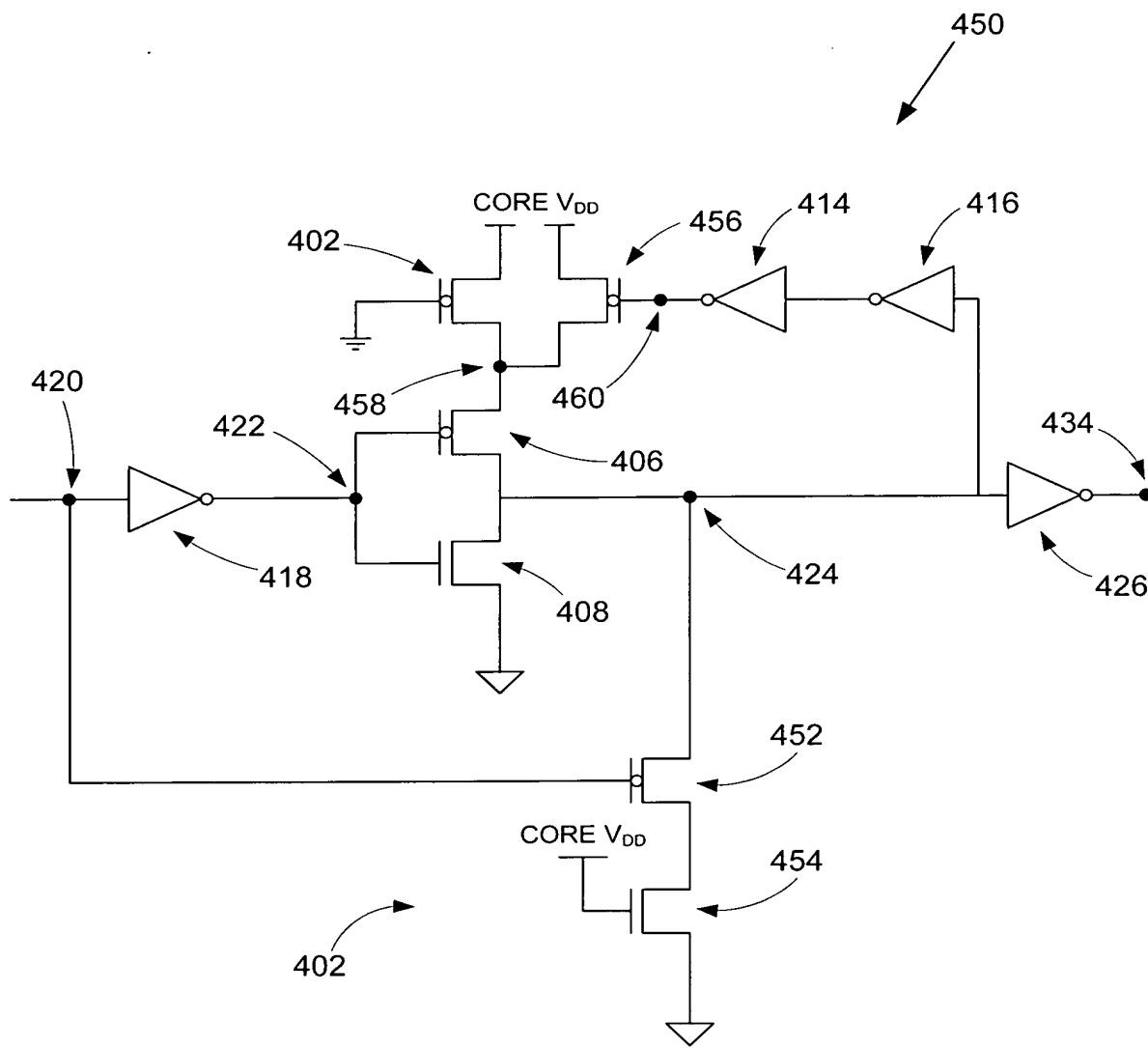


FIG. 4B

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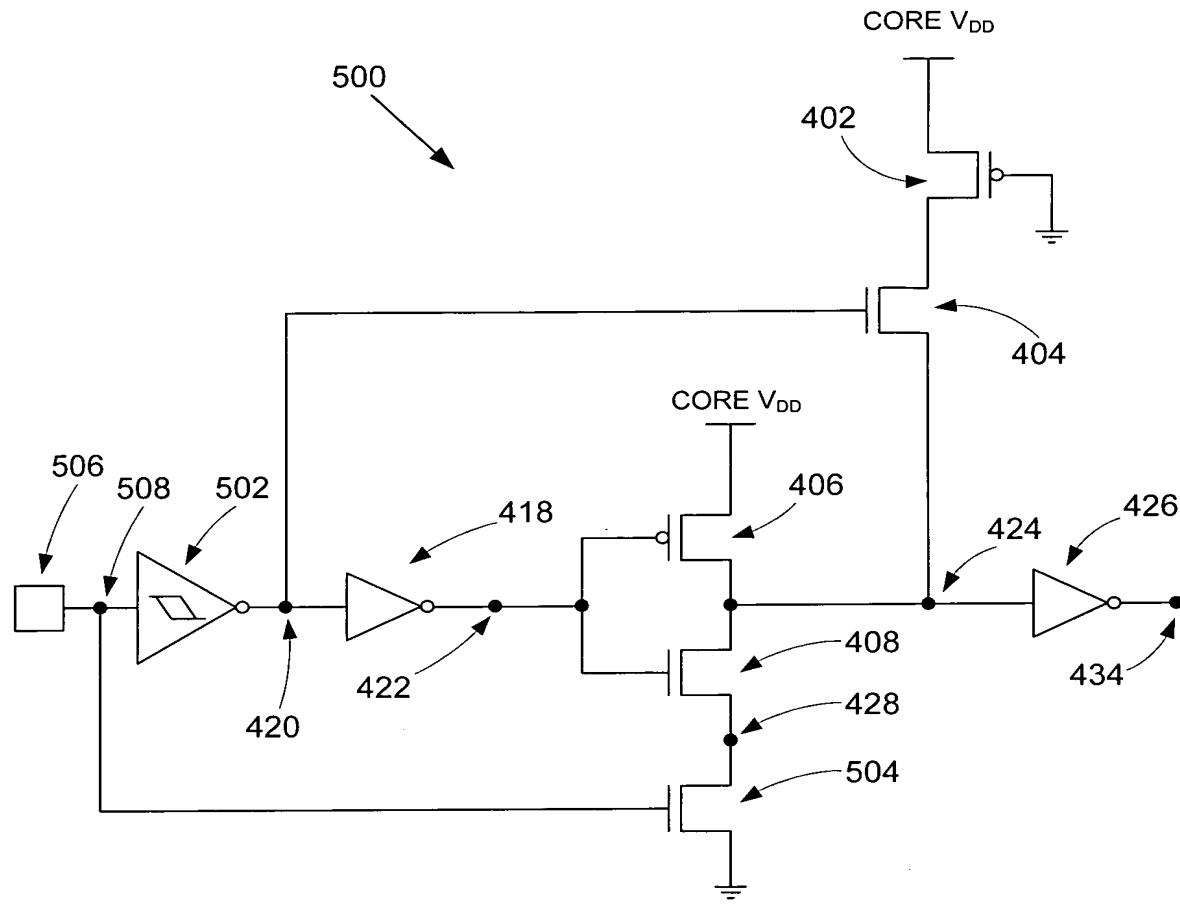


FIG. 5

REPLACEMENT SHEET

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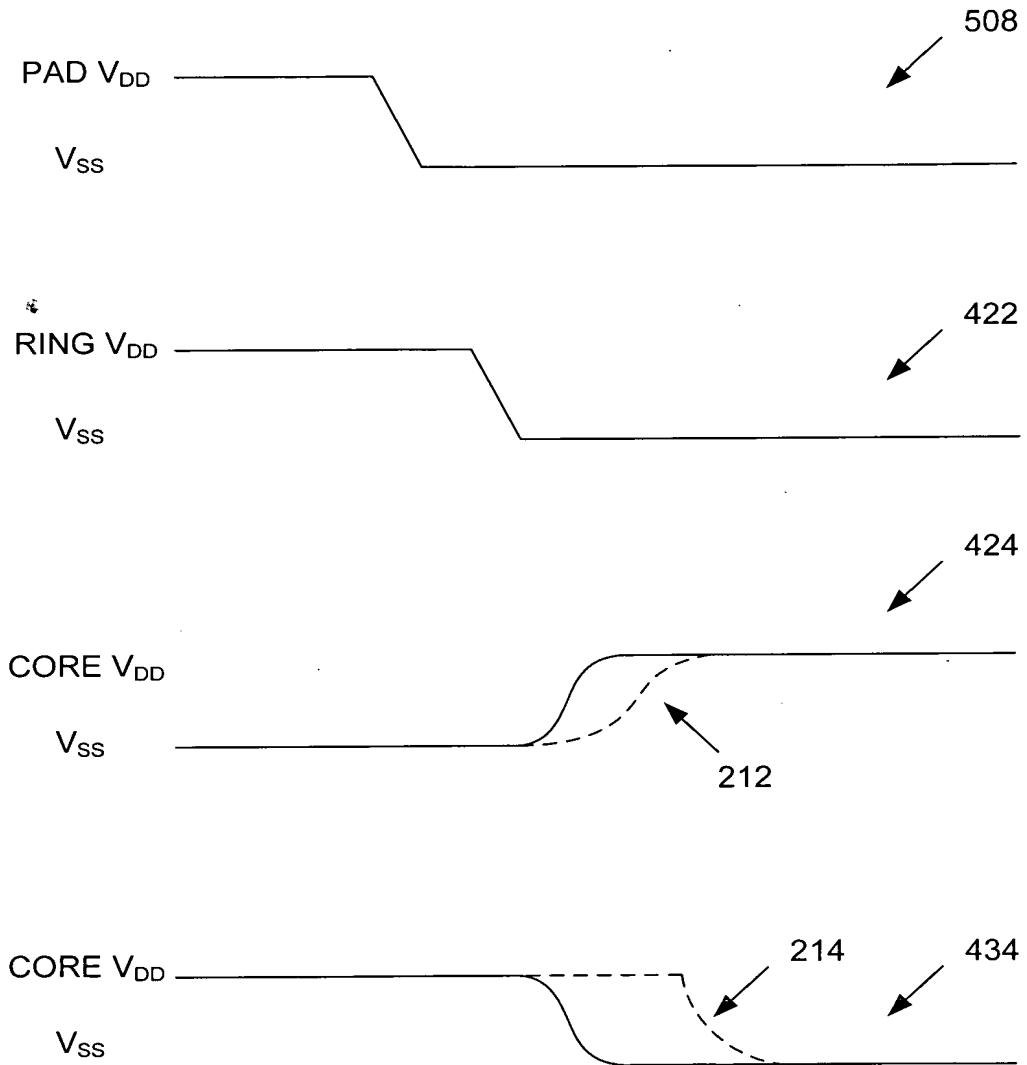


FIG. 6

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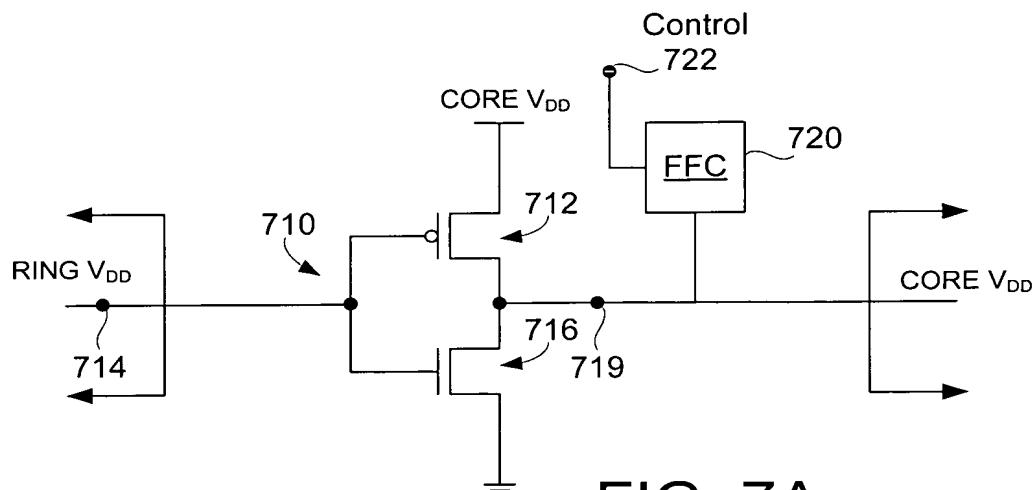


FIG. 7A

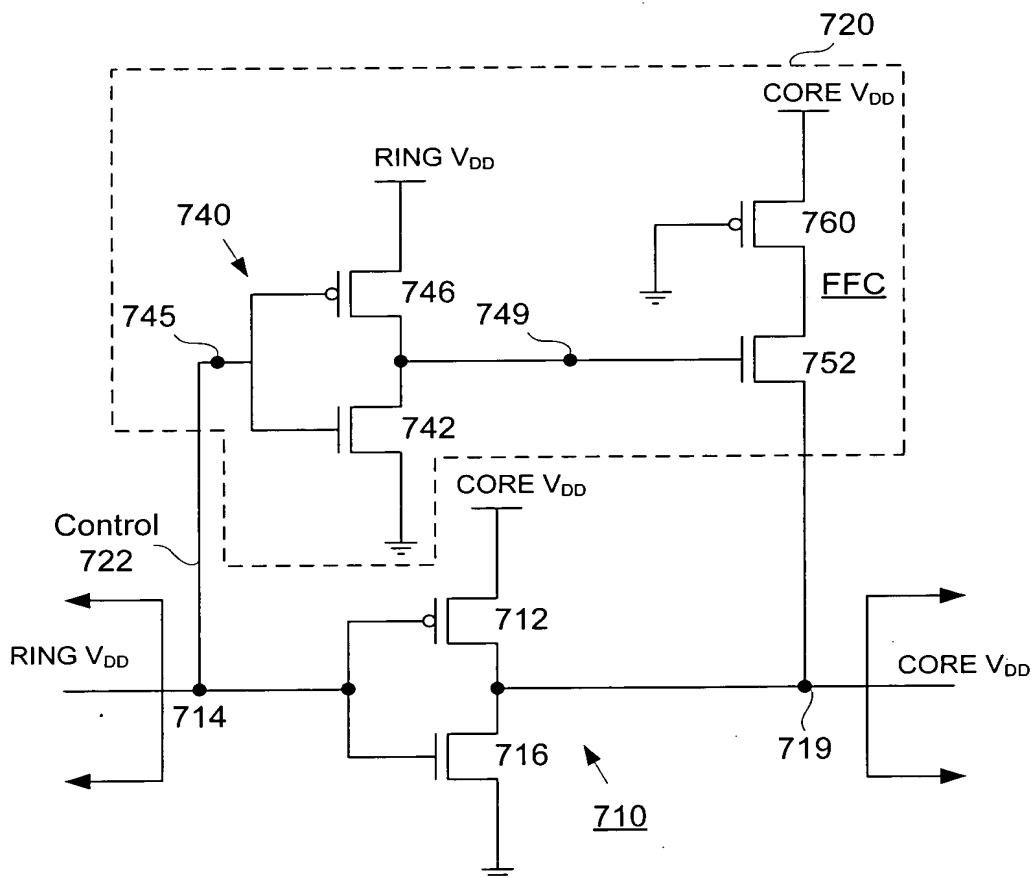


FIG. 7B